

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**EXPRESS MAIL NO. EV 327879161 US**

Applicant : Klaas Bult, et al.  
Application No. : Not Assigned  
Filed : Herewith  
Title : ANALOG TO DIGITAL CONVERTER

Grp./Div. : Not Assigned  
Examiner : Not Assigned

Docket No. : 50851/RJP/B600

**INFORMATION DISCLOSURE STATEMENT  
FOR A CONTINUING NON-CPA APPLICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
October 17, 2003

Commissioner:

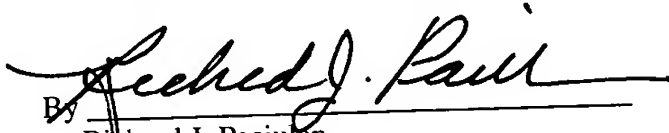
In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, enclosed is  
FORM PTO/SB/08A/B.

The references listed thereon are not enclosed because they were of record in the parent  
application, Application No. 10/146,259, filed May 15, 2002.

It is respectfully requested that these references be considered in the examination of this  
application, and identified in the list of references cited on the patent issuing on this application.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By   
Richard J. Paciulan  
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626/795-9900

RJP/cah  
Enclosure(s)  
CAH PASS18350.1-\* -10/17/03 10:09 AM

<b>FORM PTO/SB/08A/B (10-01)</b> Substitute for PTO-1449A/B  <b>INFORMATION DISCLOSURE</b>  <b>STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)	<b>Attorney Docket Number</b>	<b>50851/RJP/B600</b>
	<b>Application Number</b>	Not Assigned
	<b>Filing Date</b>	Herewith
	<b>Applicant(s)</b>	Klaas Bult, et al.
	<b>Group Art Unit</b>	Not Assigned
	<b>Examiner Name</b>	Not Assigned

### U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - kind code <sup>2</sup> . (If known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
		4,058,806	11/15/1977	Nadler
		5,157,397	10/20/1992	Vernon
		5,175,550	12/29/1992	Kattmann et al.
		5,416,485	05/16/1995	Lee
		5,675,341	10/07/1997	Vallancourt et al.
		5,710,563	01/20/1998	Vu et al.
		5,856,800	01/05/1999	Le Pailleur, et al.
		6,169,510	02/02/2001	Bult, et al.

### FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)

### OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		K. KATTMAN, et al., "A Technique for Reducing Differential Non-Linearity Errors in Flash A/D Converters", ISSCC91/Session 10/High-Speed Data Acquisition/Paper TP 10.4, Feb. 14, 1991, pp. 170-171, 1991 IEEE International Solid-State Circuits Conference
		K. KUSUMOTO, et al., "A 10-b 20-MHz 30-mW Pipelined Interpolating CMOS ADC", <i>IEEE Journal of Solid-State Circuits</i> , Dec. 1993, pp.1200-1206, Vol. 28, No. 12
		M. ITO, et al., "A 10 bit 20 MS/s 3 V Supply CMOS A/D Converter", <i>IEEE Journal of Solid-State Circuits</i> , Dec. 1994, pp. 1531-1536, Vol. 29, No. 12
		K. NAKAMURA, et al., "An 85 mW, 10 b, 40 Msample/s CMOS Parallel-Pipelined ADC", <i>IEEE Journal of Solid-State Circuits</i> , Mar. 1995, pp.173-183, Vol. 30, No. 3

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STATEMENT BY APPLICANT**

(use as many sheets as necessary)

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**Group Art Unit** Not Assigned

**Examiner Name** Not Assigned

**OTHER DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		R. VAN DE PLASSCHE, "Integrated Analog-to-Digital and Digital-to-Analog Converters", <i>The Kluwer International Series in Engineering and Computer Science; Analog Circuits and Signal Processing</i> , 1994, pp. 217-222, Kluwer Academic Publishers, The Netherlands
		R. VAN DE PLASSCHE, "Integrated Analog-to-Digital and Digital-to-Analog Converters", <i>The Kluwer International Series in Engineering and Computer Science</i> , 1994, pp. 148-187, Kluwer Academic Publishers, The Netherlands

EXAMINER SIGNATURE	DATE CONSIDERED
<p>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at <a href="http://www.pto.gov">www.pto.gov</a> or MPEP 901.4. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English Language Translation is attached.</p>	

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